

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,934,338 B1  
DATED : August 23, 2005  
INVENTOR(S) : Moshe Bublil et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [73], Assignee, reads “**Sony Corporation**, Tokyo (JP); **Sony Electronics Inc.**, Park Ridge, NJ (US)” and should read -- **Sony Corporation**, Tokyo (JP); **Sony Electronics Inc.**, Park Ridge, NJ (US) --.

Item [56], **References Cited**, OTHER PUBLICATIONS, reads:

“Patent Office, *PCT International Search Report Form PCT/ISA210*” and should read -- Patent Office, *PCT International Search Report Form PCT/ISA/210* --.  
“Seung Bae Choi et al.” reference, reads “...*A Fast Huffman Decoder*”, IEEE Transactions On Con [line break] sumer Electronics, US, IEEE Inc. New York, vol. 41, Feb. ...” and should read -- ...*A Fast Huffman Decoder*”, IEEE Transactions On Con [line break] sumer Electronics, US, IEEE Inc. New York, vol. 41, Feb. ... --. (missing the hyphen on Consumer at the line break)

Column 1.

Line 58, reads “...quantized chrominance and luminance pel values that are...” and should read -- ...quantized chrominance and luminance pel values that are... --.

Column 2.

Line 3, reads “...pel blocks followed by quantization, zigzag scan, and...” and should read -- ...pel blocks followed by quantization, zigzag scan, and... --.

Line 9, reads “The, I-frames are independently coded and are the least...” and should read -- The I-frames are independently coded and are the least ... --.

Line 31, reads “...chrominance an luminance pel values. These values are then...” and should read -- ...chrominance and luminance pel values. These values are then... --.

Column 5.

Line 36, reads “...subpicture, video information which may incorporate...” and should read -- ...subpicture video information which may incorporate... --.

Line 57, reads “...each of the functional units in ASIC 100 via two main busses,...” and should read -- ...each of the functional units in ASIC 100 via two main busses,... --.

Column 6.

Line 61, reads “...which must be a parsed and decoded by video decoder 104...” and should read -- ...which must be parsed and decoded by video decoder 104... --.

Column 8.

Lines 13-14, read, “...1/0”, has 33 selectors to provide shifting to the left of 1 bits or no shift at all.” and should read -- ...1/0”, has 33 selectors to provide shifting to the left of 1 bit or no shift at all. --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11.

TABLE 2 reads, “vbi\_mvfs1 0x08 1 r/w motion vertical field select one” and should read -- vci\_mvfs1 0x08 1 r/w motion vertical field select one --.

TABLE 3 reads, “→00:8-bit, 01:9-bit, 10:10-bit” and should read -- →00:8-bit, 01:9-bit, 10:10-bit, 11:11-bit --.

TABLE 4 reads, “vci\_dmvfifo\_adr 0x23 3 r/w dmb\_fifo read/write address” and should read -- vci\_dmvfifo\_adr 0x23 3 r/w dmv\_fifo read/write address --.

Column 13.

TABLE 6, reads “vld\_pic\_hdr r/w [31:0]] picture level params:” and should read -- vld\_pic\_hdr r/w [31:0] picture level params: --.

Column 15.

Line 59, reads, “...the shift r circuit includes a plurality of selector circuits for...” and should read -- ...the shifter circuit includes a plurality of selector circuits for... --.

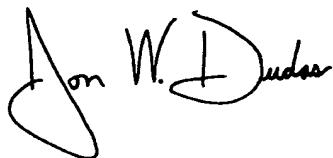
Column 16.

Line 53, reads, “...unique prefix patterns, associated with the variable...” and should read -- ...unique prefix patterns associated with the variable... --.

Line 56, reads, “...able length ncoded data to each of the plurality of...” and should read -- ...able length encoded data to each of the plurality of ... --.

Signed and Sealed this

Sixteenth Day of May, 2006



JON W. DUDAS  
Director of the United States Patent and Trademark Office